

REMARKS**INTRODUCTION:**

In accordance with the foregoing, claims 1 and 2 have been cancelled without prejudice or disclaimer, claims 3-4 have been amended, and new claim 5 has been added. No new matter is being presented, and approval and entry are respectfully requested.

Claims 3-5 are pending and under consideration. Reconsideration is respectfully requested.

OBJECTIONS TO THE DRAWINGS:

In the Office Action, at pages 2-3 numbered paragraphs 3-4, the drawings were objected to.

A. The Examiner submitted that FIGs. 5 and 6 should be designated by a legend such as ---Prior Art---.

The label ---PRIOR ART--- has been added to FIGs. 5-6. Thus, replacement sheets for FIGs. 5 and 6 are believed to overcome the objections.

B. The Examiner objected to the drawings, submitting that the terminology "the control signal" and "the clock from a control signal" must be shown or the features cancelled from the claims.

Claims 3-4 have been copied to the specification by the addition of three paragraphs (see above) on page 8 at line 8. In addition, although it is submitted that it is clear to one skilled in the art that "the control signal" is specified in the specification (see page 5, line 28 through page 6, line 1, of the specification), examples of same are noted in the paragraph added (shown underlined below), and a corresponding label has been added to FIG. 3. No new matter was added. In particular, a portion of the third paragraph added recites:

In one embodiment, the converter comprises a synchronous oscillator and a waveform shaping circuit, wherein the synchronous oscillator is synchronized with a clock output from the signal generator and generates a clock having higher frequency than that of the clock output from the signal generator, and the waveform shaping circuit shapes a control signal (such as, for example, the write enable control signal WE1), a data signal (such as, for example, data signal DIN1) and an address signal output from the signal generator into the control signal, the data signal and the address signal that have the width corresponding to the clock output from the synchronous oscillator, wherein the clock output from the synchronous oscillator and the control signal, the data signal and the address signal output from the waveform shaping circuit are provided to the

AMENDMENTS TO THE DRAWINGS:

The attached Replacement Sheets include changes to FIGs. 5 and 6 to add the legend --
-PRIOR ART---.

Approval of these changes to FIGs. 5-6 is respectfully requested.

A Replacement Sheet for FIG. 3 is included herewith to show more clearly, as is known
to those skilled in the art, that the signals represented in FIG. 3 include a "control signal."

Approval of the changes to FIG. 3 is respectfully requested.

semiconductor device.

Thus, since amended FIG. 3 clearly illustrates a control signal (such as, for example, the write enable signal WE1), a data signal (such as, for example, data signal DIN1), an address signal (such as, for example, row address signal AD(R)1), and a control signal (for example, CLK2), it is respectfully submitted that applicants should not be required to cancel this subject matter from the claims.

Replacement figures for FIGs. 3, 5 and 6 have been submitted herewith. Therefore, the outstanding drawing objections should be resolved.

Reconsideration and withdrawal of the outstanding objections to the drawings are respectfully requested.

CHANGES TO THE SPECIFICATION:

The Examiner objected to the specification, submitting that the specification failed to provide proper antecedent basis for the claimed subject matter, and that correction of "the waveform shaping circuit shapes a control signal," "the clock from a control signal," and "the control signal" is needed.

Claims 1 and 2 have been cancelled without prejudice or disclaimer. Claims 3 and 4 have been amended for clarity.

It is respectfully submitted that the copying of amended claims 3-4 to the specification by the addition of a paragraph (see above) on page 8 at line 8 clearly provides antecedent basis for "the waveform shaping circuit shapes a control signal" and "the control signal." The terminology "the clock from a control signal" has been cancelled. Thus, no further correction is needed.

As noted above, amended claims 3-4 were copied into the specification at page 8, line 8, and it is respectfully submitted that the waveform shaping circuit shapes a "control signal" so that the "control signal" is now recited in the specification. Claim 4 has been amended for clarity.

The Examiner also pointed out that correction is needed for:

a/ on page 3, line 22, "FIG. 1a and 1b" should be ---FIGs. 1a and 1b---.

b/ on page 4, lines 28 and 29, "generator 3" should be ---generator 1---.

c/ on page 5, line 8, "signal s" should be ---signals---.

The corrections suggested by the Examiner on pages 3, 4 and 5 have been implemented. Thus, the specification is now submitted to be in allowable form. No new matter

has been added.

CLAIM OBJECTIONS:

Claims 2 and 3 were objected to because of informalities.

Claim 2 has been cancelled without prejudice or disclaimer. Thus, the objection to claim 2 is now moot.

Claim 3 has been amended for clarity, and is now submitted to be in allowable form.

REJECTION UNDER 35 U.S.C. §102:

In the Office Action, at pages 4-5, numbered paragraph 10, claims 1-3 were rejected under 35 U.S.C. §102(b) as being anticipated by Nakata et al. (USPN 5,825,193; hereafter, Nakata). This rejection is traversed and reconsideration is requested.

Claims 1 and 2 have been cancelled without prejudice or disclaimer.

Nakata discloses that the frequency converter (13) is formed on the semiconductor substrate (85) having a plurality of semiconductor integrated circuits (see lines 12 to 16, column 4). However, Nakata et al. does not disclose a converter that is added at the output of the signal generator and located outside of the semiconductor devices, as is recited in amended claim 3 of the present invention. Burn-in tests according to the present invention can be performed freely, for example, for a wide variety of general purpose memories by using the converter located outside of the semiconductor device.

Thus, amended claim 3 is submitted not to be anticipated under 35 U.S.C. §102(b) by Nakata et al. (USPN 5,825,193). Since claim 4 depends from amended claim 3, claim 4 is submitted not to be anticipated under 35 U.S.C. §102(b) by Nakata et al. (USPN 5,825,193) for at least the reasons that amended claim 3 is submitted not to be anticipated under 35 U.S.C. §102(b) by Nakata et al. (USPN 5,825,193).

ALLOWABLE SUBJECT MATTER/NEW CLAIM:

In the Office Action, at page 6, numbered paragraph 11, claim 4 was objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

New claim 5 is claim 4 rewritten in independent form, including the limitations of base claim 3, and is thus submitted to be in allowable form.

CONCLUSION:

In accordance with the foregoing, it is respectfully submitted that all outstanding objections and rejections have been overcome and/or rendered moot, and further, that all pending claims patentably distinguish over the prior art. Thus, there being no further outstanding objections or rejections, the application is submitted as being in condition for allowance which action is earnestly solicited.

If the Examiner has any remaining issues to be addressed, it is believed that prosecution can be expedited by the Examiner contacting the undersigned attorney for a telephone interview to discuss resolution of such issues.

If there are any underpayments or overpayments of fees associated with the filing of this Amendment, please charge and/or credit the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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